In place of U. S. DEPARTMENT OF COMMERCE PTO-1449 PATENT AND TRADEMARK OFFICE Form  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)  SHEET 1 OF 1				Applican Applican Art Unit	m((s) Wu et al.				
	U. S. PATENT DOCUMENTS								
Examiners	Secite 18	**/Document/Number#	U.S.PA	ion Date	Locuments Applicantiol/Cited Document				
* Initials	No		1 Q HIM D	emme en	Name of Patentee or Applicantiol (cited Documents 4/4)				
MP	AA	5,854,510		-1998	Sur, Jr. et al.				
MP	AB	5,882,998	**	-1999	Sur, Jr. et al.				
MP	AC	6,242,790	1	-2001					
MP	AD	6,436,738	_1	0-2002 Yu et al.					
ME	AE AF	6,642,601		-2003 -2003	1				
<u> </u>	AF	6,670,824	12-30	-2003	Goodbread et al.				
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FOREIGN PATENT DOCUMENTS							
Examiners	Cite c	Foreign Patenti	Rublication Date)	Patentee or/Applicant of Cited 14	At a Translation A		
Initials	NO:	Country Code = Number = Kinn)	200	Ratentee or Applicant of (Cited ) Document	NAME OF THE PARTY		
					A CONTRACTOR OF THE PROPERTY O		

OTHER PRIOR ART							
Examiners Cite	Indude name of the lauthor (in:CAPITAIS LETTERS) (title of the article stitle of (the item, date in page (s), volume to the individual street in t						
\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	ALAVI, et al., A PROMElement Baset on Saliciate Agailbmeration of Poly Fases in a CMOS Logic Process, December 1997, 4 pgs. IEEE International Electron Detrice Meeting.						
	AllCHAEL W. Electrical Fluse Structure for Low Voltage And Single Core Applications, December 16/2003, 4 pgs. Taiwan Semiponductor Manufacturing Company, Ud.						
	V						

Examiner	4	+	Date	0/15
Signature	1/	1	Considered	1 9705
	1/	1		

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DAG and AH references not considered because they are not in the PTO file.